

**IN THE SPECIFICATION:**

Please replace numbered paragraph 0031, spanning pages 9 and 10, with the following paragraph:

-- **[0031]** Figure ~~for~~ 4 illustrates an example arrangement of a multi-scalable streaming data server in accordance with the embodiment of the present invention. As illustrated in Figure 4, the various elements can be easily seen as corresponding to their respective elements in Figure 1. In step 1 of Figure 4, incoming streaming data is transferred to the input buffer IN\_BUF of the memory 430 by the network interface card NIC0. In step 2, the server thread A of the processor CPU0 reads data out of the input buffer IN\_BUF using a non-temporal prefetch (bypassing the L2 cache 410). In step 3, the server thread A copy's data to the output buffer OUT\_BUF, (a streaming store which bypasses the L2 cache 410 and therefore results in efficient L2 cache utilization). In step 4, the network interface card NIC3 generates a hardware interrupt which is serviced by the processor CPU3 and the DPC callback is registered. In step 5, the DPC handler actually executes on the processor CPU3 (interrupt/DPC binding). In step 6, the DPC handler invokes server thread B which runs on processor CPU3 (server thread/buffer affinity). In step 7, the server thread B completes packet assembly and locks the output buffer OUT\_BUF ready for the network interface card NIC3 to read. Lastly, in step 8, the network interface card NIC3 reads data out of the output buffer OUT\_BUF and streams it to the clients connected thereto. --